



10/697504

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: NAIKI Examiner: Unknown
Serial No.: 10/697504 Group Art Unit: Unknown
Filed: October 30, 2003 Docket No.: 12844.0050US01
Title: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE

CERTIFICATE UNDER 37 CFR 1.8:

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail, with sufficient postage, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on February 4, 2004.

By: A Ewald

Name: A Ewald

SUBMISSION OF FORMAL DRAWINGS

Mail Stop Missing Parts

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Dear Sir:

Submitted herewith are 6 sheet(s) of Formal Drawings for completion of this application, and in compliance with the requirements of the Notice to File Corrected Application Papers dated January 29, 2004.

Respectfully submitted,

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By: Douglas P. Mueller

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Dated: February 4, 2004

DPM/ame

